SEP 0 9 2005 W

Mail Certificate

I hereby confirm that I, Kevin D., McCarthy, have deposited this correspondence along with other documents with the U.S. Postal Service by first class mail on September 7, 2005 to Commissioner for Patents, P.O. Box 1450, Alexandria,

VA 22313-1450.

Kevin D. McCarthy

Patent 0-04-107

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor:

Ben-Yaakov.

Serial no.:

10/502,249

Filed:

July 22, 2004

371(c) date:

December 27, 2004

Title:

LOW FREQUENCY INVERTER FED BY A HIGH

FREQUENCY AC CURRENT SOURCE

Examiner:

N/A

Art Unit:

N/A

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir/Madam:

Information Disclosure Statement under 37 C.F.R. 1.98

Applicant submits the enclosed information disclosure statement that is being filed prior to the first office action. Accordingly, it is applicant's opinion that the examiner at the U.S. Patent and Trademark Office should consider these references in determining the patentability of this application.

If Applicant becomes aware of other relevant references that are not cumulative of the submitted references, Applicant will submit them.

Respectfully submitted

Kevin D. McCarthy

Reg. No. 35,278

Roach, Brown, McCarthy & Gruber, P.C. 1620 Liberty Building - 420 Main Street Buffalo, New York 14202

PTO/SB/08A (07-05)
Approved for use through 07/31/2006. OMB 0651-0031
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid OMB control number.

ubstitute for form 1449/PTO

Sheet 1

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known				
Application Number	10/502,249			
Filing Date	12/27/2004			
First Named Inventor	Ben-Yaakov			
Art Unit	N/A			
Examiner Name	N/A			
Attorney Docket Number	0-04-107			

			U.S. PATEN	DOCUMENTS	
Examiner Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant	
		Number-Kind Code ^{2 (# known)}			Figures Appear
		^{US-} 6,038,148	03-14-2000	Farrington et al.	
		US-			

		FORE	IGN PATENT DOCL	IMENTS		
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
		Country Code ³ "Number ⁴ "Kind Code ⁵ (if known)	MM-DD-YYYY		Or Relevant Figures Appear	T°
		JP 2000262069 A2	09-22-2000	Toru	Abstract	
		WO 0167828 A1	09-13-2001	Kisaichi et al.	Abstract	
		GB 2320627 A	06-24-1998	Moriguchi et al.		
		WO 00/54398 A1	09-14-2000	Farrignton et al.		
		WO 01/67828 A1	09-13-2001	Kisaichi et al.		

Examiner		Date	
Signature		Considered	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. Applicant's unique citation designation number (optional). See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. Applicant is to place a check mark here if English language Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.



JP2000262069A2: GATE POWER SUPPLY CIRCUIT

Title: JP2000262069A2:

GATE POWER SUPPLY CIRCUIT

Country:

Kind: JP Japan

Inventor: YOSHIOKA TORU;

Assignee: SAWAFUJI ELECTRIC CO LTD Published / Filed: 2000-09-22 / 1999-03-08 Application Number: JP1999000060318 IPC Code: H02M 7/5387; H02M 7/537;

Priority Number: 1999-03-08 JP1999000060318

Abstract: JP2000262069A2:

PROBLEM TO BE SOLVED: To attain space saving and cost reduction by charging energy stored in a capacitor one after another, and supplying it to a gate driving circuit of a switching element group of a static AC-DC power conversion circuit

SOLUTION: The switching element groups FET1, 4 of a lower arm of a bridge type static power conversion circuit are driven by energy stored in the first capacitors 31, 33. Charging is conducted by the second capacitors 32, 34 through the first diodes 52, 54 from a control power source. The switching element groups FET2, 4 of an upper arm are driven by the energy. At this time, the third capacitor 63 is charged through the second diode 62 from the second capacitors 32, 34. The gate of a thyristor 10 of a static type AC-DC power conversion circuit connected with the forward stage of a bridge type DC-AC power conversion circuit is driven by the energy. It is thus possible to attain space saving and cost reduction by disuse of an insulation transformer.

COPYRIGHT: (C)2000,JPO

WO0167828A1: LAMP DEVICE AND DEVICE FOR DRIVING DISCHARGE LAMP

WO0167828A1:

LAMP DEVICE AND DEVICE FOR DRIVING DISCHARGE LAMP

Title: Lamp device and device for driving discharge lamp with an auxiliary power supply

Country:

Kind: WO World Intellectual Property Organization (WIPO)

Inventor: KISAICHI, Hiroyasu; c/o MITSUBISHI DENKI KABUSHIKI KAISHA,

2-3, Marunouchi 2-chome, Chiyoda-ku, Tokyo 100-8310, Japan ASAYAMA, Masatomi; c/o OSRAM-MELCO LIMITED, 8-29, Kita-Saiwai 2-chome, Nishi-ku, Yokohama-shi, Kanagawa 220-0004, Japan

Assignee: OSRAM-MELCO LIMITED, 8-29, Kita-Saiwai 2-chome, Nishi-ku,

Yokohama-shi, Kanagawa 220-0004, Japan

News, Profiles, Stocks and More about this company

Published / Filed: 2001-09-13 / 2001-01-26 Application Number: WO2001JP0000503 IPC Code: H05B 41/282; H05B 41/16;

ECLA Code: H02M1/00B12; H02M3/335S; H02M3/335S2; H05B41/282;

H05B41/288K2;

Priority Number: 2000-03-09 JP2000000065521

Abstract: WO0167828A1

A stepup and stepdown converter (3) includes a transformer (3a), a first switching element (3b) connected in series with the primary of the transformer (3a) connected to AC power line (1), a first diode (3c) connected with the secondary of the transformer (3a), and a first capacitor (3d). Zero cross detector means (10) detects a zero crossing point of the voltage of AC line (1). An auxiliary power circuit (4) includes a second diode (4a) connected to the connection between the transformer (3a) and the first switching element (3b). The auxiliary power circuit charges a second capacitor (4c) through a second diode (4a) with the energy stored in the primary winding of the transformer (3a), and supplies the energy stored in the second capacitor (4c) to a discharge lamp (8) through a second switching element (4b), a third diode (4f), and an inductor (4e). Based on the output from the zero cross detector means (10), a control circuit (9) drives the second switching element (4b) of the auxiliary power circuit (4) for a predetermined period before and after a zero crossing point. [French] [Japanese]

Attorney, Agent or Firm: KOBAYASHI, Hisao;